Analog Mixed-Signal and Photonic Integrated Circuits (AMPIC) Lab at the University of Idaho has openings for PhD students in multiple areas in Analog IC design starting Fall 2017.

Our lab work conducts research on cross-cutting areas that leverage advances in nanophotonics and emerging devices to create new pathways in Analog, Mixed-Signal and RF integrated systems. Topics of interest include:

- CMOS Photonic ICs for Terabit/s optical interconnects
- Millimeter-wave (mmWave) Photonic ICs and Phased-Arrays
- Next-generation Memory Interfaces
- Mixed-signal Neuromorphic ICs for Deep Learning

The position(s) require strengths in Analog IC design, Physics, analytical modeling and programming. Verbal and technical writing skills in English language are a must. Preference will be given to candidates with prior IC design and tape-out experience using Cadence, ADS, and Matlab.

Qualified candidates are welcome to apply for the position(s) by mailing their CV to Dr. Vishal Saxena (vsaxena@uidaho.edu). The position(s) will be open till it is filled.

More information can be found at:
http://webpages.uidaho.edu/vsaxena/pages/opportunities.html